

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	235	exclusive interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:22
L2	32292	"711"/\$.cls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 10:55
L3	1	l1 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 10:55
L4	271790	soc or (system adj2 "on" adj2 chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 10:56
L5	0	l1 and l4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 10:56
L6	13	exclusive near2 memory near2 interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:04
L7	86	exclusive near2 memory near2 connection	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:22
L8	4	l7 and l4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:05

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L9	13	exclusive near2 memory near2 interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:28
L10	955	shared near2 memory near2 interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:24
L11	281	shared memory interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:24
L12	45	l11 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:24
L13	140	exclusive near2 memory near2 bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:40
L14	1	10/646289	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:41
L15	0	order\$3 and l14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:40
L16	435	exclusive connection	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:51



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L17	0	I16 with memory with cache	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:54
L18	84	I16 with memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:51
L19	2	(hit or miss or misshit) with (internal near2 state) with (cpu or processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 11:57
L20	43	(hit or miss or misshit) same (internal near2 state) same (cpu or processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:04
L21	0	lru with (internal near2 state) with (cpu or processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:08
L22	1	((replac\$3 or swap\$4 or flush\$3) near2 (line or block)) with (internal near2 state) with (cpu or processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:10
L23	3	((replac\$3 or swap\$4 or flush\$3) near2 (line or block)) same (internal near2 state) same (cpu or processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:11
L24	3	((prefetch\$3 or pre-fetch\$3) near2 (line or block)) same (internal near2 state) same (cpu or processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:14



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L25	736	(internal near2 state) near2 (cpu or processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:14
L26	765	exclusive connect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:17
L27	7	switch near2 l26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:18
L28	30	switch with l26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:18
L29	26	(direct interface) near2 memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:52
L30	39	(direct near2 memory near2 connect\$3) with cache	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/11/22 12:53

